

PROFILE OF PRINCIPAL



1. Name: Dr. A.K. Aditya.
2. Date of Birth: 01.05.1960
3. Educational Qualification: B.E. Tel. E., M. Tech, Ph. D.
4. Work Experience :
 - a) Teaching: 22 Years
 - b) Research: 14 Years
 - c) Industry: 2¹/₂ Years
5. Area of Specialization: Microelectronics and VLSI.
6. Subjects teaching at U.G. Level: Microelectronics and VLSI.
7. Research guidance: Guiding one scholar jointly.
8. Projects carried out.
 - Chief Coordinator of CAD of VLSI sponsored by MHRD under the THRUST AREA. (Completed)
 - Chief coordinator of Modernisation of Computer Centre sponsored by MHRD under the Modernisation and Removal of Obsolescence. (Completed)
 - Chief Coordinator, “Modelling and Simulation of Si/SiGe of Bipolar/MOS Devices using Finite Element Method for VLSI application”, an R&D Project sponsored by MHRD. (Ongoing)
 - Chief coordinator of Object Oriented R&D Lab., Sponsored by MHRD under thrust Area.

All the above projects were sanctioned at REC, Silchar.
9. Patents: Nil
10. Technology transfer: Nil
11. Research Publications: Int. Journal: 2 Int. Cnf: 1, National Cnf: 4
12. No. of books published with details: Nil